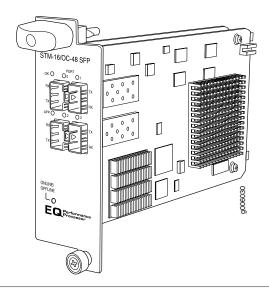
SONET/SDH OC48/STM16 Enhanced IQ (IQE) PIC with SFP



Software release

• Junos OS Release 10.4R2 and later (Type 3)

Description

- Four OC48/STM16 ports
- · Clear channel functionality
- SONET and SDH is configured on a per-port granularity
- Power requirement: 1.06 A @ 48 V (51 W)
- Weight: 1.6 lb (0.725 kg)
- Model number: PC-4OC48-STM16-IQE-SFP

Hardware features

- Ports are numbered:
 - Top row: 0 and 1 from left to right
 - Bottom row: 2 and 3 from left to right
- Maximum transmission units (MTUs) of up to 9192 bytes

Software features

- Quality of service (QoS) per channel: weighted round-robin (WRR), random early detection (RED), weighted random early detection (WRED)
- Fine-grained egress queuing per logical interface. See the Class of Service Feature Guide for Routing Devices and EX9200 Switches for more information about class-of-service features
- · Packet buffering
- · Local line and remote payload loopback testing
- · Optical diagnostics and monitoring
- Clocking options: internal or external/loop mode. Each OC48 transmitter port is configured either in internal or external mode. The default clocking option is internal mode.
- · Encapsulations:
 - Extended Frame Relay for circuit cross-connect (CCC) and translational cross-connect (TCC)
 - Flexible Frame Relay
 - Frame Relav
 - Frame Relay for CCC
 - Frame Relay for TCC
 - Frame Relay port CCC
 - High-Level Data Link Control (HDLC)
 - HDLC framing for CCC
 - HDLC framing for TCC
 - MPLS CCC
 - MPLS TCC
 - Point-to-Point Protocol (PPP)
 - PPP for CCC
 - PPP for TCC

Cables and connectors

TIP: You can use the Hardware Compatibility Tool to find information about the pluggable transceivers supported on your Juniper Networks device.

The list of supported transceivers for the MX Series is located at https://pathfinder.juniper.net/hct/category/#catKey=100001&modelType;=All&pf;=MX+Series.

NOTE: To extend the life of the laser, when a PIC is not being actively used with any valid links, take the PIC offline until you are ready to establish a link to another device. For information about taking a PIC offline, see the **request chassis pic offline** command in the CLI Explorer.